



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/936,158	02/28/2002	Yoshiaki Matsubara	SONYJP-137	9335
530	7590	11/17/2005		
LERNER, DAVID, LITTENBERG, KRUMHOLZ & MENTLIK 600 SOUTH AVENUE WEST WESTFIELD, NJ 07090			EXAMINER DHARIA, PRABODH M	
			ART UNIT	PAPER NUMBER
			2673	

DATE MAILED: 11/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

Election/Restrictions

1. **Status:** Receipt is acknowledged of papers submitted on 02-08-2005 under election, which have been placed of record in the file. Claims 1-61 are pending in this action. AS previous *Election/Restrictions* requirement were not adequate, examiner has decided to re-do *Election/Restrictions* requirements per class/subclass basis.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Specification

3. Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

4. The abstract of the disclosure is objected to because total word count exceeds 150.

Correction is required. See MPEP § 608.01(b).

Election/Restrictions

5. Restriction to one of the following inventions is required under 35 U.S.C. 121:
- I. Claims 1-9,61 are drawn to picture display device driving by controlled by data processors and input device, with memory classified in class 345, subclass 572.
 - II. Claims 10-19 are drawn to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional classified in class 345, subclass 563.
 - III. Claims 20-29 are drawn to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, in class 386 subclass 52.
 - IV. Claims 30-49 are drawn to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed classified in class 714 subclass 48.
 - V. Claims 50-60 are drawn to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed and frequency measuring means classified in class 360 subclass 13.

Art Unit: 2673

6. The inventions are distinct, each from other because:

Invention I relates to a picture display device driving by controlled by data processors and input device, with memory, however, it does not relate to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed and picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed and frequency measuring means.

Invention II relates relate to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional however, it does not relate to a picture display device driving by controlled by data processors and input device, with memory, picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that

Art Unit: 2673

an input of the operation has not been performed and picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed and frequency measuring means.

Invention III relates to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, however, it does not relate to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, a picture display device driving by controlled by data processors and input device, with memory, picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed and picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed and frequency measuring means.

Invention IV relates to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed, however, it does not relate to picture display device driving by

Art Unit: 2673

controlled by data processors and input device, with memory, and communicating bi-directional a picture display device driving by controlled by data processors and input device, with memory, picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, and picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed and frequency measuring means.

Invention V relates to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed and frequency measuring means however, it does not relate to picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, processor notifying that an input of the operation has not been performed, picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional a picture display device driving by controlled by data processors and input device, with memory, picture display device driving by controlled by data processors and input device, with memory, and communicating bi-directional, combining plurality of video signals into one screen corresponding to picture sizes, and picture display device driving by controlled by data processors and input device, with memory.

7. These above mentioned reasons the inventions described and categorized by class /subclass above are distinct. Search required for each class and subclass is independent.

8. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

9. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement is traversed (37 CFR 1.143).

Conclusion

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prabodh M. Dharia whose telephone number is 571-272-7668. The examiner can normally be reached on M-F 8AM to 5PM.

11. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

12. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications

Art Unit: 2673

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:


Commissioner of Patents and Trademarks

Washington, D.C. 20231

PD

AU2673

November 10, 2005



VIJAY SHANKAR
PRIMARY EXAMINER